

Figure 1
programmable
logic device 10

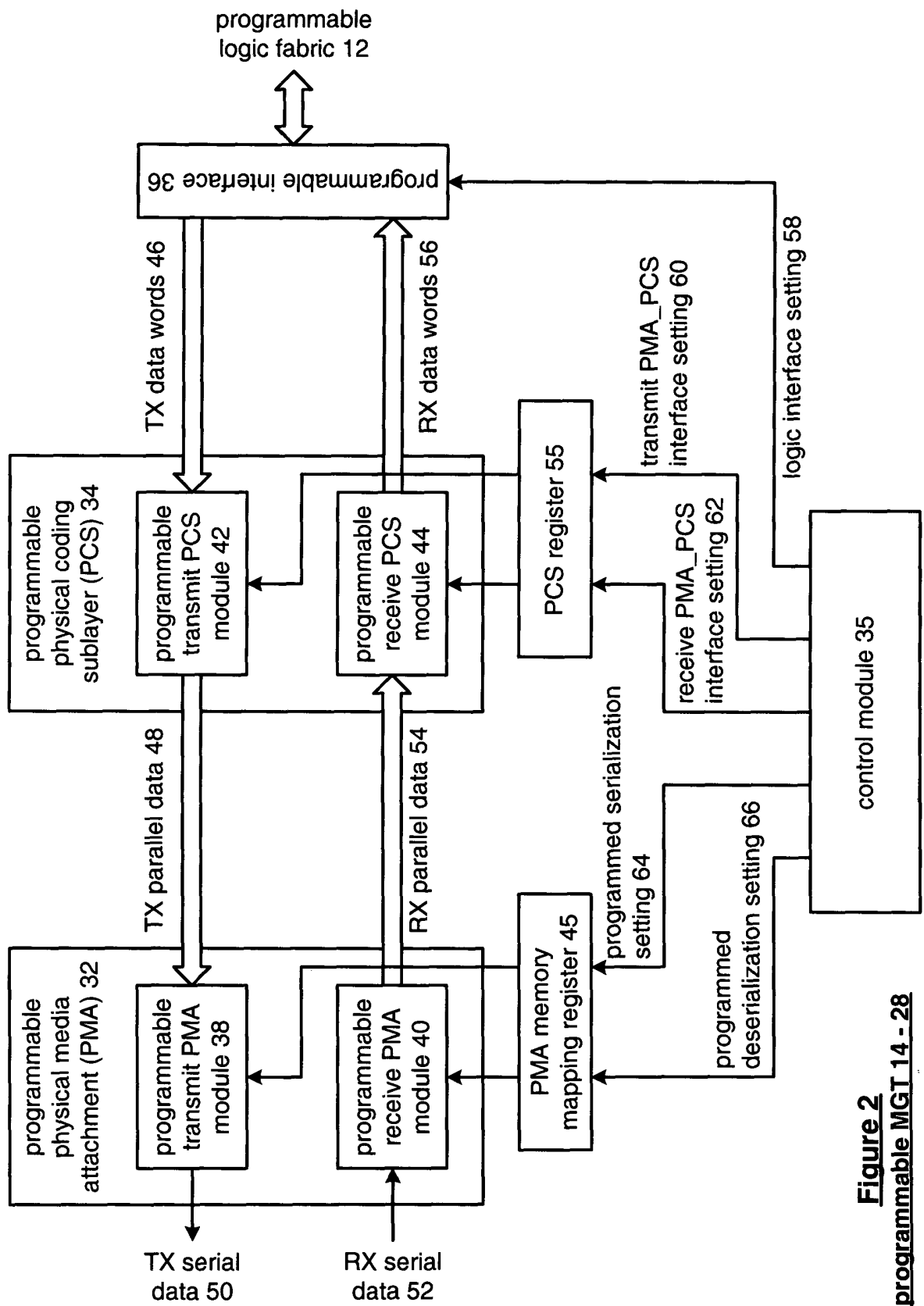


Figure 2
programmable MGT 14 - 28

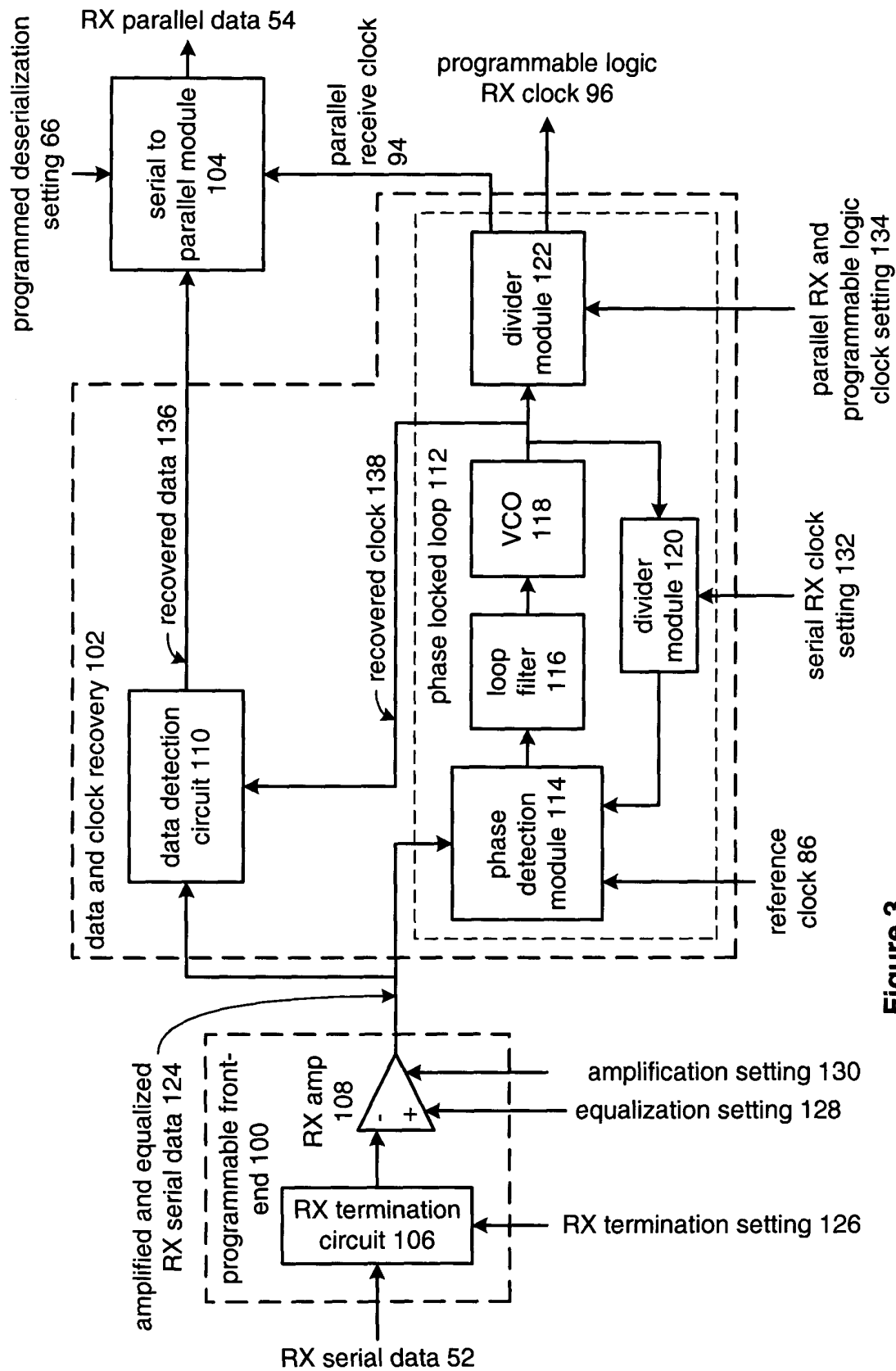


Figure 3
programmable receive
PMA module 40

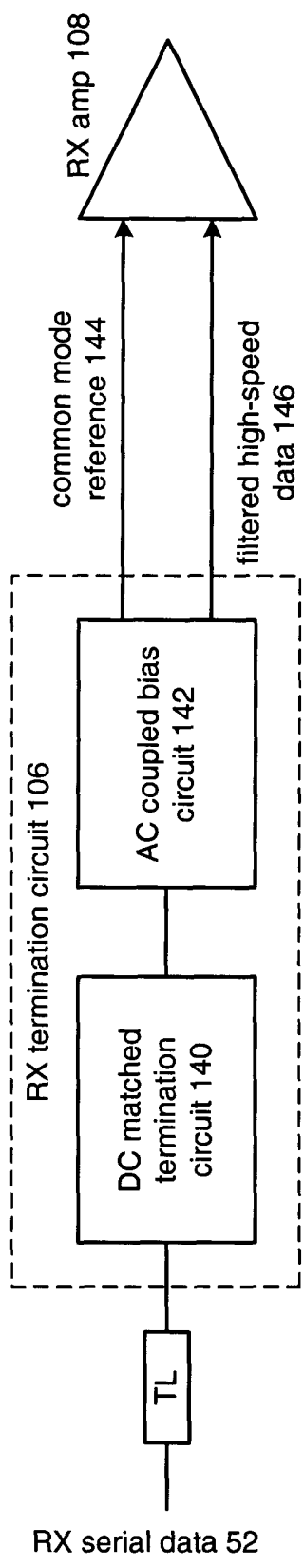


Figure 4

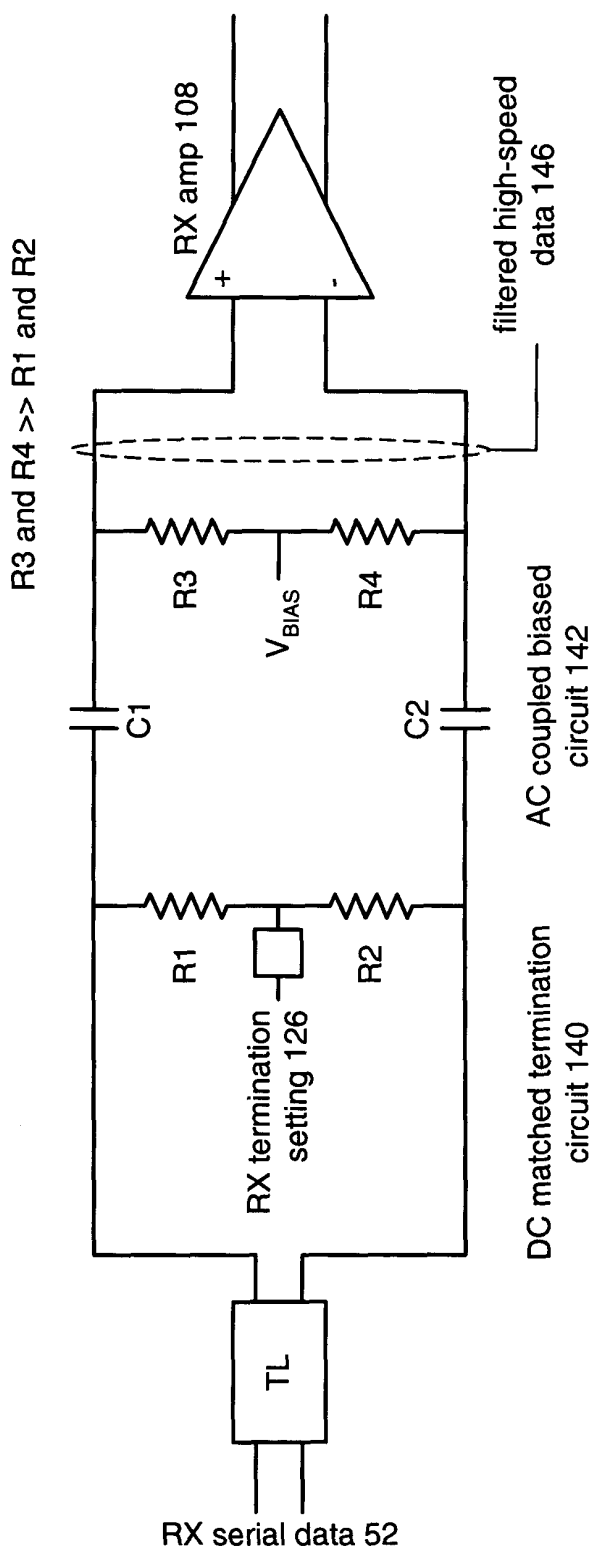


Figure 5

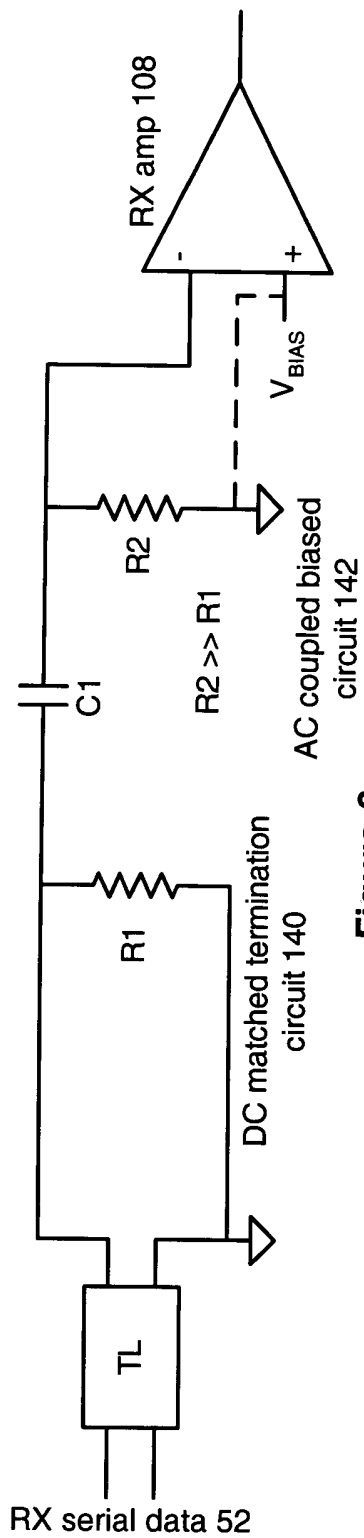


Figure 6

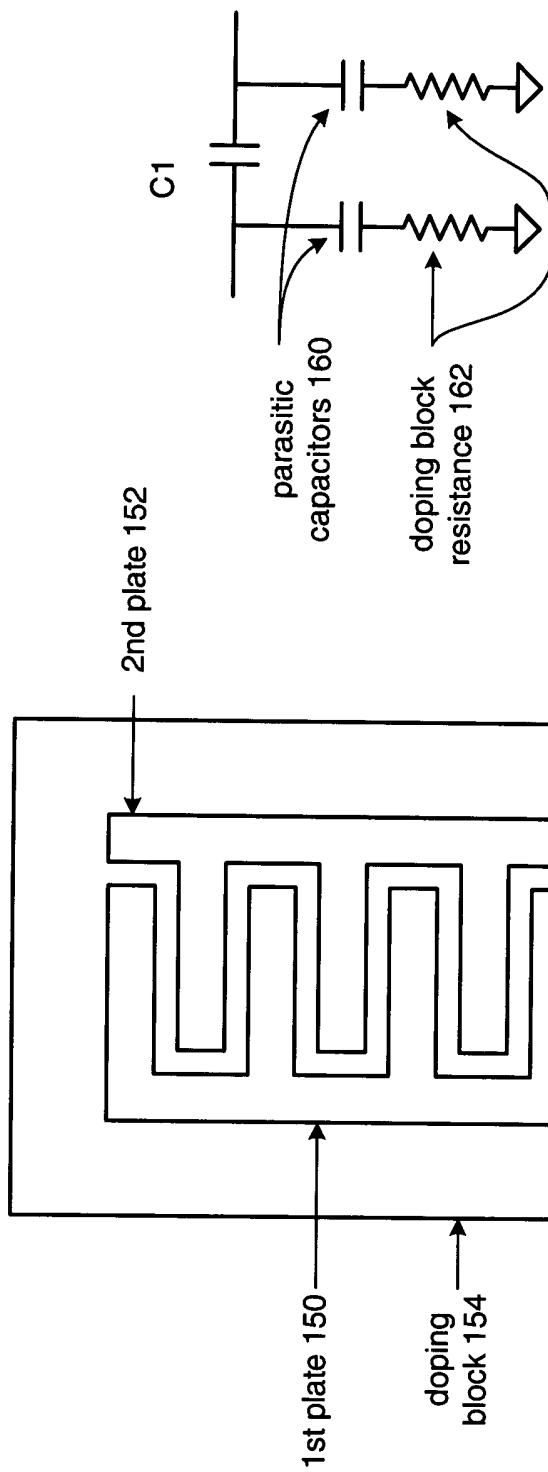


Figure 7
capacitor C1 or C2

Figure 8
equivalent circuit of capacitor of Figure 7